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** CONTINUING DATA *****

NO

** FOREIGN APPLICATIONS *****

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IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 18	TOTAL CLAIMS 28	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	EXAMINER'S SIGNATURE	INITIALS		

Verified and
Acknowledged

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TITLE
 Method of fabricating semiconductor device using Plasma-Enhanced CVD

☐ All Fees